

CLAIMS

1. An integrated circuit including an on-board system clock, the integrated circuit including a clock filter configured to determine a temperature of the integrated circuit and to alter an output of the system clock based on the temperature.
2. An integrated circuit according to claim 1, wherein the clock filter is configured to alter the output of the system clock in the event the temperature is outside a predetermined temperature range.
3. An integrated circuit according to claim 2, wherein altering the output includes preventing the clock signal from reaching one or more logical circuits on the integrated circuit to which it would otherwise be applied.
4. An integrated circuit according to claim 3, wherein the predetermined temperature range is selected such that a temperature-related speed of the system clock output that is not due to the clock filter is within a predetermined frequency range.
5. An integrated circuit according to claim 4, wherein the frequency range is within an operating frequency of some or all of the logic circuitry to which the system clock is supplied.
6. An integrated circuit according to claim 4, wherein the temperature range is bounded at a lower level such that an output of the system clock is prevented from reaching some or all of the logic circuitry prior to race conditions due to low temperature causing unpredictable logical behaviour.